Subject : I. T. - Elective-II c) Embedded Systems Design & Programming

Day : Wednesday

Date : 14/12/2016

S.D.E. |||||||||||||||||||||
33536

Time : 02.00 P.M. TO 05.00 P.M.

Max Marks : 80   Total Pages : 1

N.B.:

1) Attempt **ANY FOUR** questions from Section – I and attempt **ANY TWO** questions from Section – II.
2) Answers to both the sections should be written in the **SAME** answer book.
3) Figures to the right indicate **FULL** marks.

SECTION – I

Q.1   Explain the role of DAC and ADC in Embedded System.   **[12]**

Q.2   What is programmable logic devices? Explain in detail.   **[12]**

Q.3   Discuss in detail Embedded Operating Systems.   **[12]**

Q.4   What is Target hardware platform? Explain in detail.   **[12]**

Q.5   Explain the role of Lactches and Buffers in embedded system design.   **[12]**

SECTION – II

Q.6   Write a assembly language program to receive data from Port 1 and send it to **[16]** Port 2.

Q.7   Discuss the internal architecture of 8051 microcontroller in detail.   **[16]**

Q.8   Write short notes on **ANY TWO** of the following: **[16]**
  a) MCS – 51 KIT
  b) Program Memory and Data Memory
  c) Reset circuit

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